

Docket No. 500.30310CX2

ATTENTION: BOX AF

JAN 2 2 2003

Technology Center 2100

EXPEDITED PROCEDURE REQUESTED

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the US patent application of:

Applicant: Nobuo HAMAMOTO et al.

Serial No.: 09/886,368

Filed:

22 June 2001

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For:

DIGITAL INFORMATION SYSTEM, DIGITAL AUDIO

SIGNAL PROCESSOR AND SIGNAL CONVERTER

Art Unit:

2123

Examiner:

K. Thangavelu

AMENDMENT AFTER FINAL REJECTION

Assistant Commissioner for Patents Washington, D.C. 20231

17 January 2003

Sir:

In response to the Office Action mailed 18 September 2002 in connection with the above-identified application, Applicant respectfully submits the following amendments and remarks.

IN THE CLAIMS:

Please enter amended replacement Claims 55 and 59, as follows.

55 (Qnce Amended) A memory apparatus having a playback circuit removably connected with a digital signal source to store digital data received from said digital signal source and to reproduce the digital data stored therein independently of said digital source, comprising:

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